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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/762,781

01/23/2004

Pengfei Zhang

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6127

20350 7590 10/01/2007  
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EXAMINER

BOWERS, BRANDON

ART UNIT

PAPER NUMBER

2825

MAIL DATE

DELIVERY MODE

10/01/2007

PAPER

**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

# Office Action Summary

Application No.

10/762,781

Applicant(s)

ZHANG ET AL.

Examiner

Brandon W. Bowers

Art Unit

2825

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 13 February 2007.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 2 and 5 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 2 and 5 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 23 January 2004 is/are: a) ☒ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: \_\_\_\_\_

## **DETAILED ACTION**

### ***Election/Restrictions***

Applicant's election without traverse of group 2, claims 2 and 5 in the reply filed on 13 February 2007 is acknowledged.

### ***Specification/Drawings***

37 CFR 1.74 states in part "the detailed description of the invention shall refer to the different views by specifying the numbers of the figures, and to the different parts by use of reference letters or numerals". The drawing and specification are both objected to for failing to meet this requirement and both need to be amended to add reference numerals.

### ***Claim Rejections - 35 USC § 112***

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 2 and 5 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. With regards to claim 2, the claim is in an unrecognized format that is confusing and ambiguous. It appears to be a method claim but has no steps to be performed, but just an extended preamble that does not carry patentable weight. Furthermore, with regards to claims 2 and 5, they are written in a manner that is punctually and grammatically incorrect which further leads to confusion.

Art Unit: 2825

For purposes of the following rejections, this claim 2 is being treated as a signal flow circuit physical synthesis method comprising a steps of tracing a signal flow, and placing/routing a circuit physical layout including a high priority critical signal path automatically by a software program.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 2 is rejected under 35 U.S.C. 102(b) as being anticipated by Kim et al. "A Timing-driven Data Path Layout Synthesis with Integer Programming".

Kim teaches a signal flow circuit physical synthesis method (see page 716 section entitled Introduction) comprising a steps of tracing a signal flow (see page 716 section entitled Data Path Synthesis) and placing/routing a circuit physical layout including a high priority critical signal path automatically by a software program (see pages 716-719 sections entitled Placement and Routing).

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and

Art Unit: 2825

the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kim et al.

"A Timing-driven Data Path Layout Synthesis with Integer Programming" in view of Long, VLSI Handbook, Ch. 71, "Logic Design Principles and Examples," CRC/IEEE Press, 2000.

Kim teaches the signal flow circuit physical synthesis method of claim 2 as described above. Kim further teaches applying a signal flow driven circuit analysis technique comprising tracing a circuit signal flow so that analyzing and partitioning a circuit is based on functionality/criticality, and generating circuit layout constraints are performed by a software program (see pages 716-718 section entitled Placement). Kim further teaches generating a layout constraint on the critical nodes (see page 717, section entitled initial placement). Kim does not teach wherein the generating a layout constraint on the critical nodes uses the open circuit time constant technique. Long teaches using the open circuit time constant technique (see section 10.3.2.3).

Accordingly it would have been obvious for one skilled in the art at the time of invention to incorporate the teachings of Long into the teachings of Kim to create a method comprising all of the limitations of claim 5 because the open circuit time constant technique is widely used for the analysis of the bandwidth of analog electronic circuits (see Long, section 10.3.2.3).

**Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brandon W. Bowers whose telephone number is (571)272-1888. The examiner can normally be reached on 8:30 am until 5:00 pm Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jack Chiang can be reached on (571)272-7483. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

BWB

  
JACK CHIANG  
SUPERVISORY PATENT EXAMINER